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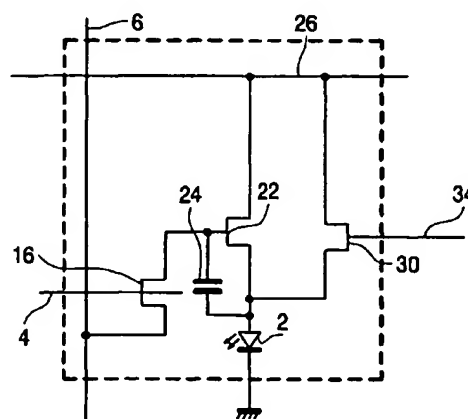
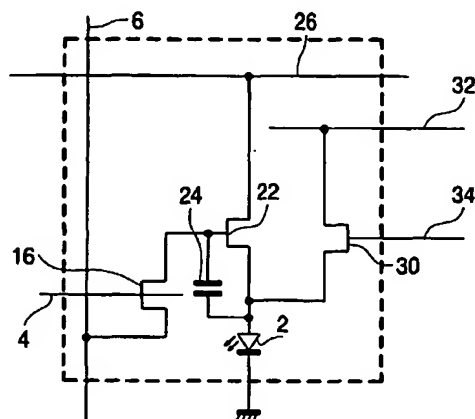
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(54) Title: ELECTROLUMINESCENT DISPLAY DEVICE HAVING PIXELS WITH NMOS TRANSISTORS



(57) Abstract: An active matrix electroluminescent display device has pixels using an amorphous silicon or microcrystalline silicon drive NMOS transistor (22) connected between the anode of the display element (2) and a power supply line (26). A storage capacitor (24) is connected between the anode of the display element and the gate of the drive transistor (22). An amorphous silicon or microcrystalline silicon second drive NMOS transistor (30) supplies a holding voltage to the anode of the display element (2). This arrangement enables the voltage across the display element to be held while the transistor gate drive voltage is stored on the storage capacitor. This enables an accurate current source pixel circuit to be implemented using NMOS transistors.

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For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER
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B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the International search (name of data base and, where practical, search terms used)

EPO-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
P, X	WO 03 001496 A (IBM) 3 January 2003 (2003-01-03) page 10, line 24 -page 12, line 14; figure 3	1, 9
A	EP 1 220 191 A (SAMSUNG SDI CO LTD) 3 July 2002 (2002-07-03) paragraph '0044!; figure 10	1, 9
A	EP 1 130 565 A (SONY CORP) 5 September 2001 (2001-09-05) figure 5	1, 9

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Information on patent family members

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